D STATES PATENT AND TRADEMARK OFFICE

In re application of:

Examiner:

Anne KASZYNSKI & Jacques ABILY

Serial No.: 10/627,976

Group Art Unit:

Filed: July 28, 2003

Corresponding to:

For: Method For Functional Verification Of An

French Patent

Integrated Circuit Model For Constituting A Verification Platform, Equipment Emulator

Application FR 02 09691

McLean, Virginia

And Verification Platform

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

In compliance with the duty of disclosure set forth in 37 CFR 1.56, it is requested that the documents listed on the attached Information Disclosure Statement By Applicant be made of record in this application. These documents were cited in the corresponding French application No. FR 02 09691 filed on July 30, 2002. A copy of the "Rapport De Recherche Preliminaire" (Preliminary Search Report) is enclosed. In compliance with the requirements of 37 CFR 1.97 and 1.98, the possible relevance of the documents is noted on the attached Information Disclosure Statement by Applicant. Copies of the documents are enclosed herewith.

Without any assertion of prior art effect, the documents on the attached Information Disclosure Statement by Applicant are called to the Examiner's attention. Although the relevance of each document has been briefly indicated, the Examiner is

urged to make his own determination of relevance and no representation is made nor intended that "better" art is not available.

Respectfully submitted,

MILES & STOCKBRIDGE P.C.

Date

March 25, 2004

By:

Edward J. Kondracki Registration No. 20,604

Customer No. 181

Miles & Stockbridge, P.C. 1751 Pinnacle Drive, Suite 500 McLean, Virginia 22102-3833

Tel.: (703) 903-9000

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		D 16	OL COLLDE	Application Number	10/627,976			
INF	ORMATION	DIS	CLOSURE	Filing Date	July 28, 2003			
STA	TEMENT B	Y: A	PPLICANT	First Named Inventor	Anne KASZYNSKI ET AL.O			
			_	Art Unit		7		
	(use as many shee	ets as	necessary)	Examiner Name				
Sheet	1	of	1	Attorney Docket Number	T2147-908627	MAR 2 5 2004		

			U.S. PAT	ENT DOCUMENTS	E S
Examiner Initials	Cite No.1	Document Number Page 1	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columber Lines, Where Relevant Passages 4 255 Avent Figures Appear
	AA	US- 10/073,461 XYA	June 13, 2002	HOLLANDER	Page 1, par. 11, page 3, pars.
		US-			41-43; page 4, par. 62-pg 5, par
		US-			86; page 7, par. 132, pg. 8, par
		US-			159; pg. 11, par 226, figs 1,2
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	AB	US- 5,732,247 A	March 24, 1998	PLOUFFE JR., GEO. ET AL.	Col. 1 line 14-20
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	FOREIGN PATENT DOCUMENTS								
Examiner Initials*	Cite No.1	Foreign Patent Document Office ³ Number ⁴ Kind ⁵ (if known)				Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	Τg
	BB	wo	02 08966		Α	Jan. 31, 2002	Bollano, Gianmario; Turolla,	Pg. 6, line 21-27	
							Maura; Ettorre, Donato	Pg. 9, line 26-pg. 10	
								line 17; pg. 16, line	
								22-pg. 17, line 2	
								T	

Examiner	Date	
Signature	Considered	

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. ¹ Applicant's unique citation designation number (optional). ² See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.

This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.